

wherein said first memory is a read only memory (ROM) which is not re-writable;

a second memory to store a second program;

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concl.* an input unit which inputs an encrypted second program from outside the IC card; and

a microprocessor according to said first program, applies said decryption function to said encrypted second program, and then writes a decrypted second program, which is executable by said microprocessor, in said second memory,

wherein, when said encrypted second program is not encrypted correctly, said second program is not written in said second memory. --

REMARKS

Entry of the above amendments prior to examination is respectfully requested.

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Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



Carl I. Brundidge
Registration No. 29,621

CIB/jdc
(703) 312-6600